

Figure 1 - Linux SMP host

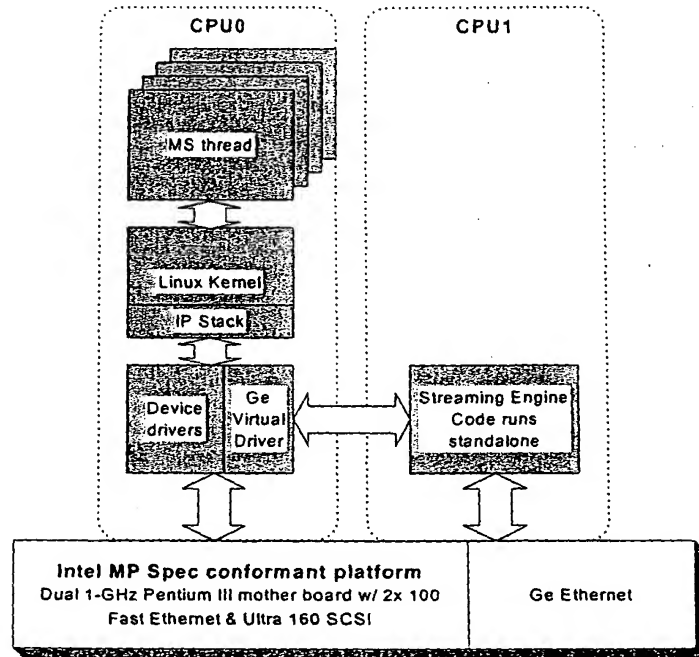


Figure 2 - Uni-processor Linux

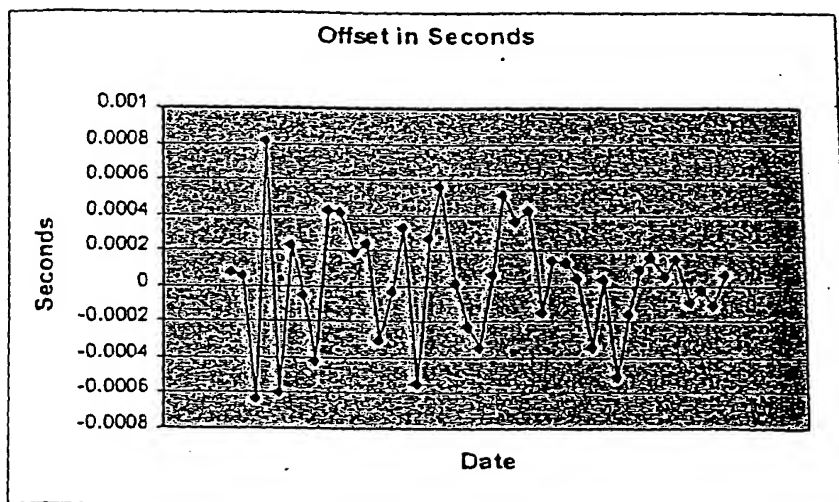


FIG. 3 - Offset in seconds from time source

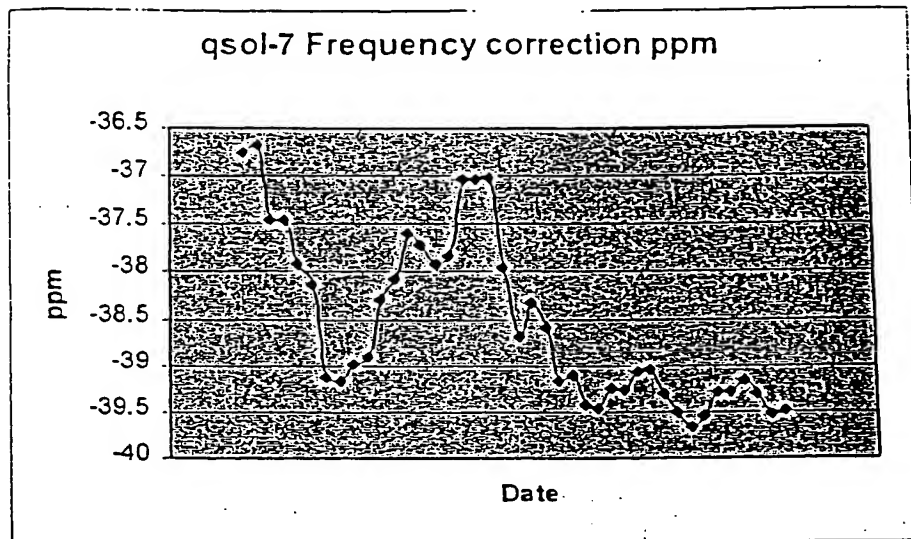


FIG 4 - Frequency correction in ppm

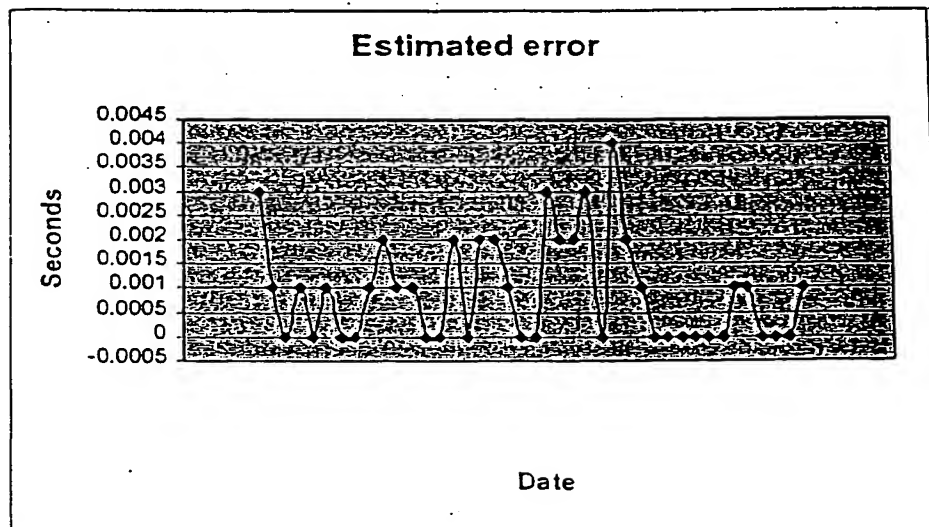


FIG 5 - Estimated error in seconds

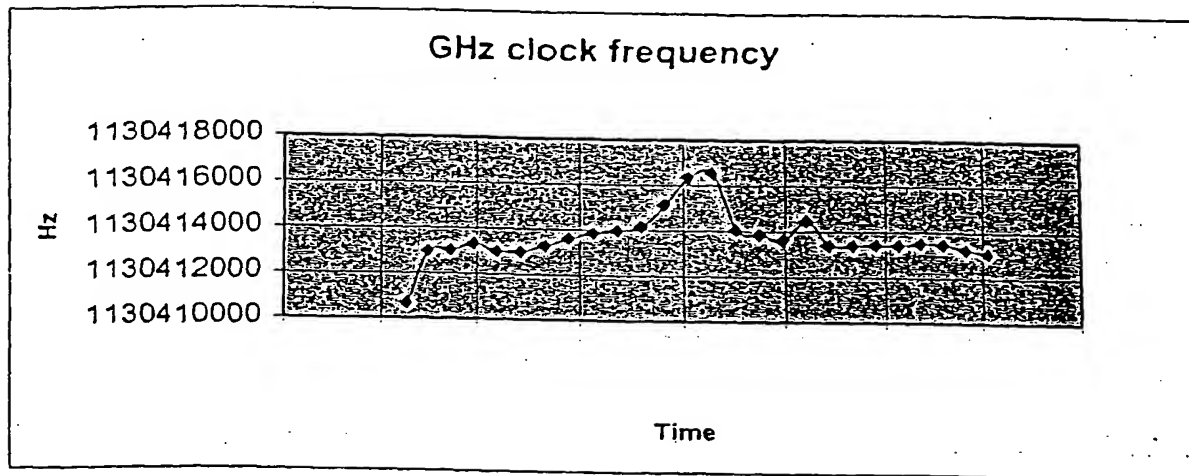


FIG. 6 - GHz Clock Frequency

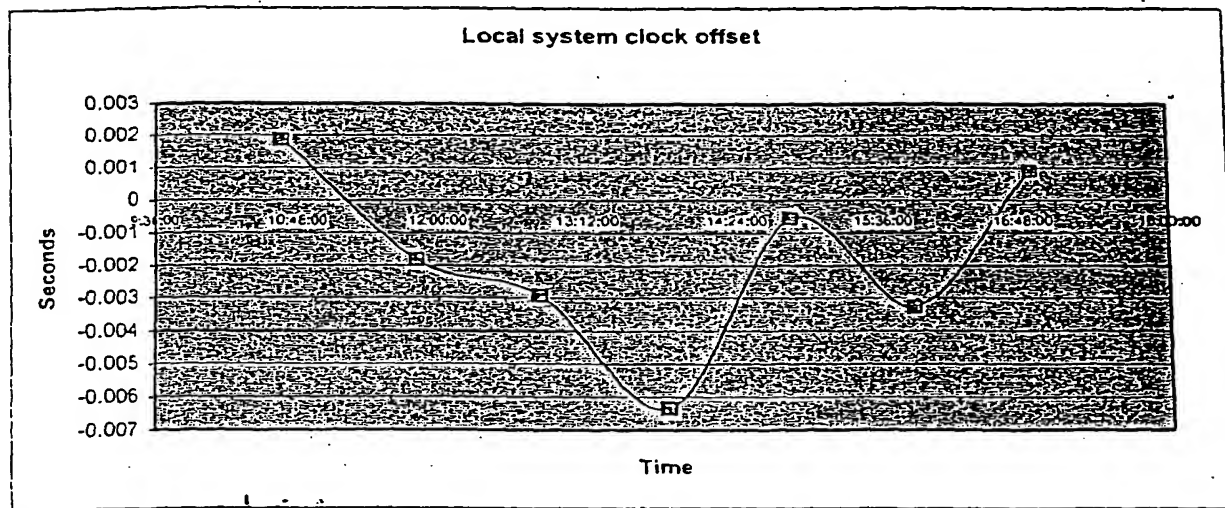


FIG. 7 - Local System Clock Offset

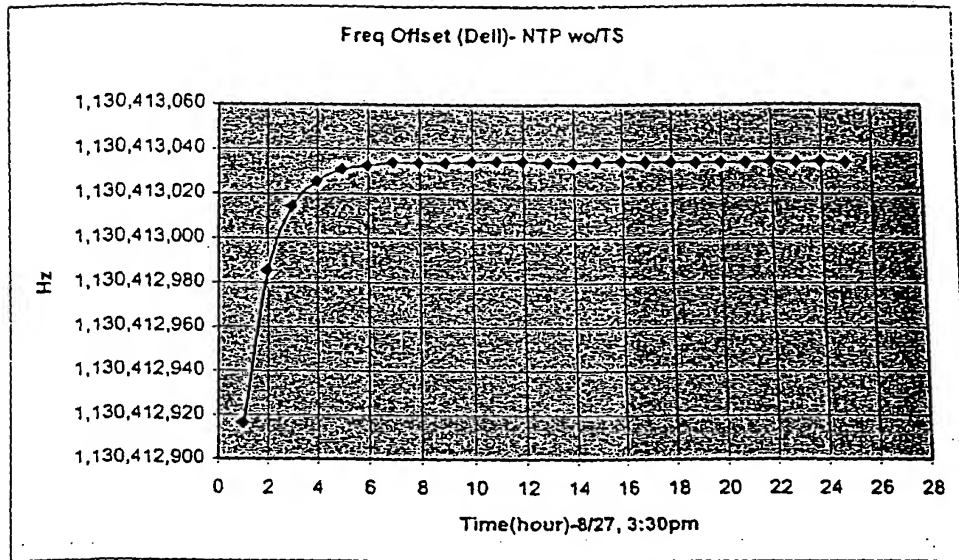


FIG. 8 NTP correction after timeserver removed

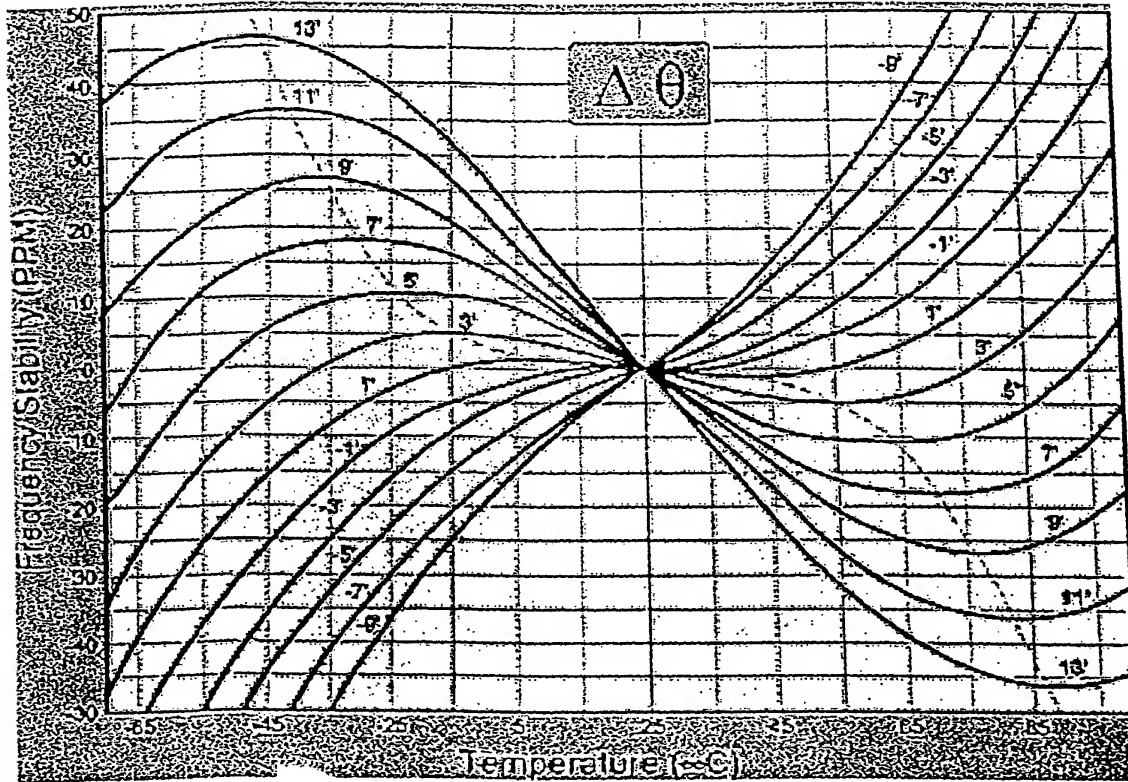


FIG. 9 - Crystal drift with temperature

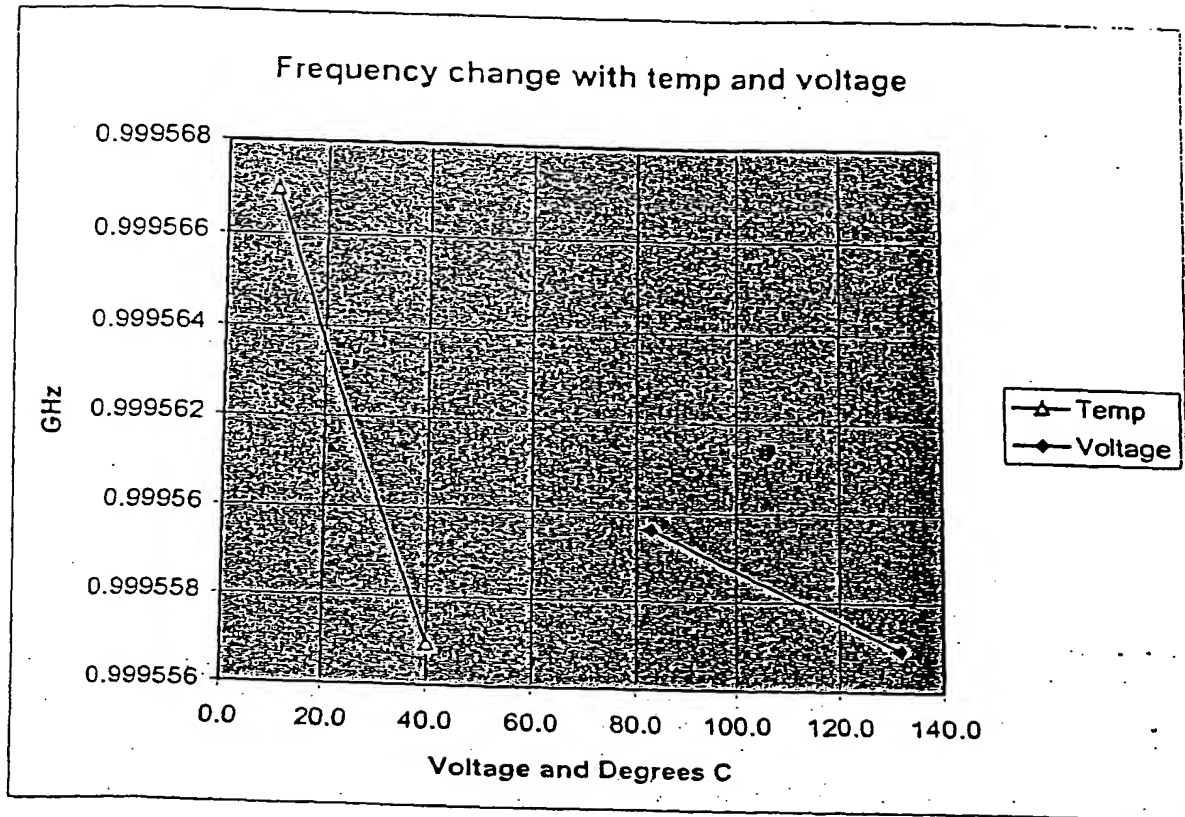


FIG. 10 - Change of frequency with Temperature and Supply voltage

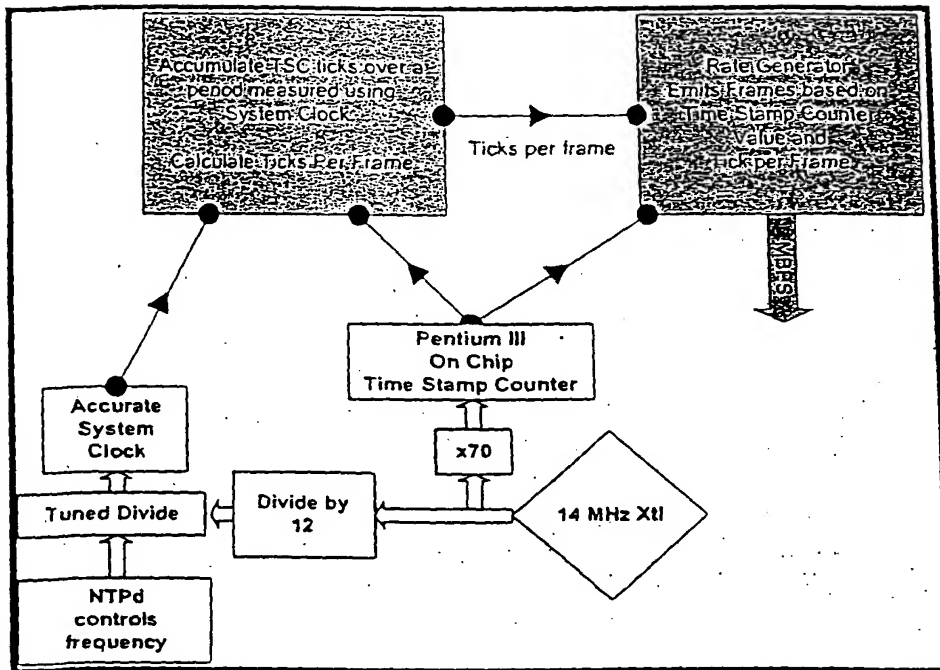


FIG. 11 - Rate controller control loop

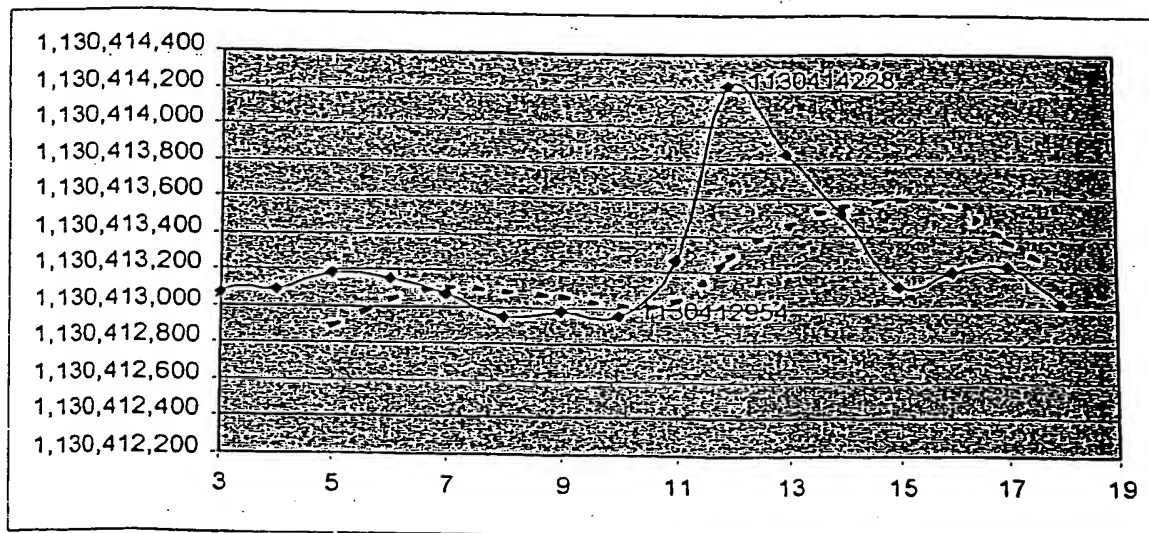


FIG. 12 - Low pass filter of GHz crystal measurements

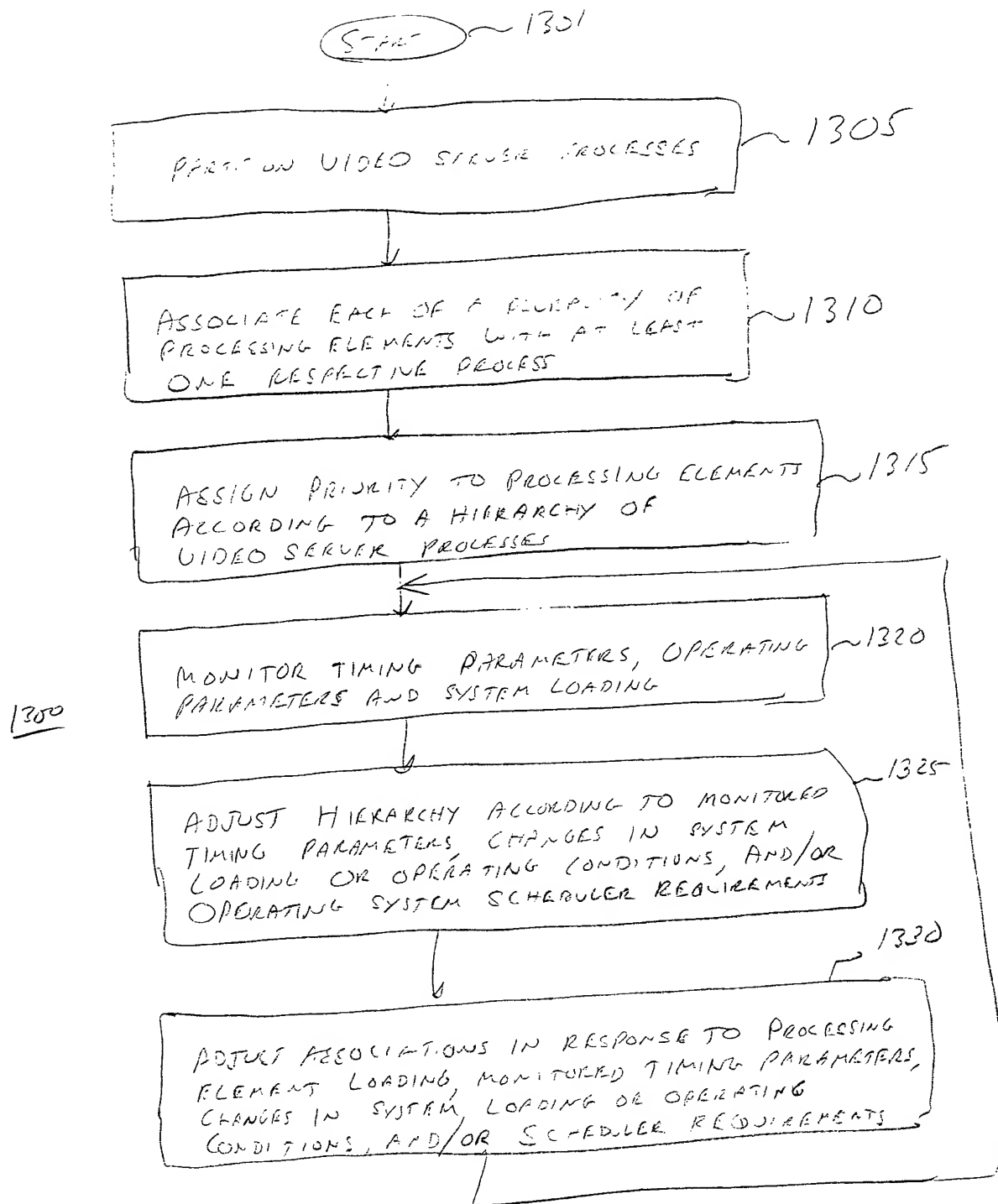


FIG 13

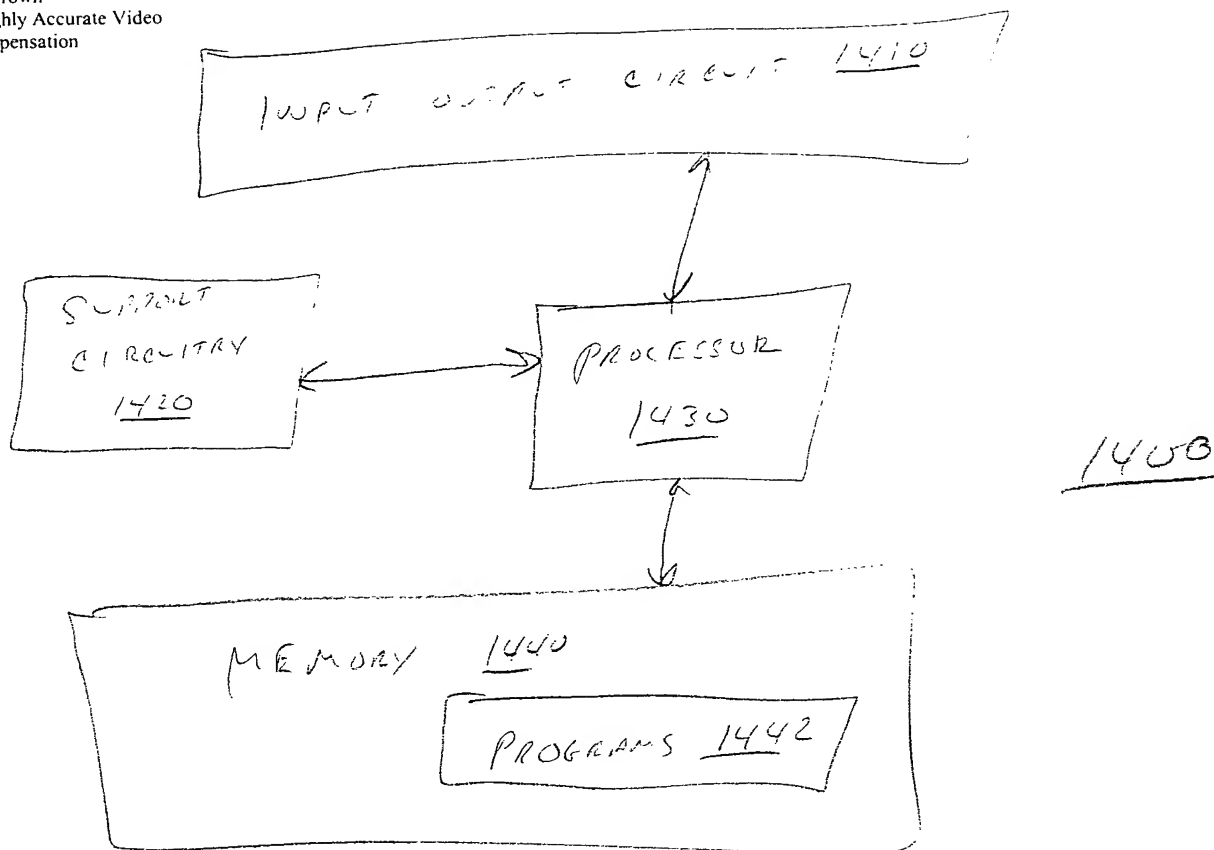


FIG. 14